

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In application of: Shah et al.

Attorney Docket No.: PETAP003

Application No.: 09/834,964

Examiner: GREY, Christopher

Filed: April 13, 2001

Group: 2667

Title: SWITCH FABRIC WITH EFFICIENT  
SPATIAL MULTICAST

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on January 20, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

Agnes Spence

**INFORMATION DISCLOSURE STATEMENT  
BEFORE FINAL ACTION OR NOTICE OF ALLOWANCE  
(37 CFR §§ 1.56 AND 1.97(c))**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, a copy of which is attached, may be material to examination of the above-identified patent application. Applicants submit this reference in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make this citation of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that this reference indeed constitutes prior art.

This Information Disclosure Statement is being filed after the mailing date of the first Office Action on the merits, or after three months of the filing date of this application, whichever event occurred last, but it is believed before the mailing date of either: (i) a final action under § 1.113 or (ii) a notice of allowance under § 1.311, whichever occurs first.

Accompanying this Information Disclosure Statement is

- ☐ a statement as specified in 37 CFR 1.97(e); or
- ☒ the fee set forth in 37 CFR 1.17(p).

The undersigned hereby states:

☐ that each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application no more than three months prior to the filing of the Information Disclosure Statement; or

☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

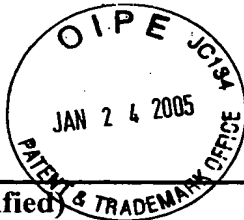
If fees are due, enclosed is our Check No. 25094 for \$180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. PETAP003).

Respectfully submitted,  
BEYER WEAVER & THOMAS, LLP



C. Douglass Thomas  
Registration No. 32,947

P.O. Box 70250  
Oakland, CA 94612-0250  
(650) 961-8300



<b>Form 1449 (Modified)</b> <b>Information Disclosure Statement By Applicant</b> (Use Several Sheets if Necessary)	Atty Docket No. PETAP003 Applicant: Shah et al. Filing Date April 13, 2001	Application No.: 09/834,964  Group 2667

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	2002/0012340	01/2002	Kalkunte et al.			
	B	2002/0012341	01/2002	Battle et al.			
	C	2002/0013621	09/2002	Battle et al.			
	D	2002/0027908	03/2002	Kalkunte et al.			
	E	2003/0097467	05/2003	Sano			
	F	2003/0118016	06/2003	Kalkunte et al.			
	G	2003/0198231	10/2003	Kalkunte et al.			
	H	4,965,788	10/1990	Newman			
	I	5,467,347	11/1995	Petersen			
	J	5,475,682	12/1995	Choudhury et al.			
	K	5,544,168	08/1996	Jeffrey et al.			
	L	5,703,879	12/1997	Proctor et al.			
	M	5,790,522	08/1998	Fichou et al.			
	N	5,790,539	08/1998	Chao et al.			
	O	6,067,286	05/2000	Jones et al.			
	P	6,118,761	09/2000	Kalkunte et al.			
	Q	6,144,635	11/2000	Nakagawa, Tatsuo			
	R	6,147,996	11/2002	Laor et al.			
	S	6,246,692	06/2001	Dai et al.			
	T	6,438,106	08/2002	Pillar et al.			
	U	6,438,132	08/2002	Vincent et al.			
	V	6,535,510	03/2003	Kalkunte et al.			
	W	6,542,507	04/2003	Khacherian et al.			
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. PETAP003	Application No.: 09/834,964
	Applicant: Shah et al. Filing Date April 13, 2001	Group 2667

#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	X	6,567,417	05/2003	Kalkunte et al.			
	Y	6,603,771	08/2003	Raza, S. Babar			
	Z	6,636,510	10/2003	Lee et al.			
	A1	6,643,256	11/2003	Shimojo et al.			
	B1	6,658,016	12/2003	Dai et al.			
	C1	6,680,910	01/2004	Ni, Jie			
	D1	6,721,273	04/2004	Lyon, Norman A.			

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	E1	"Common Switch Interface Specification-L1, version 1.0", CSIX, August 5, 2000, <a href="http://www.csix.org">http://www.csix.org</a> .
	F1	M. Shreedhar and George Varghese, "Efficient Fair Queuing Using Deficit Round-Robin", IEEE/ACM Transactions on Networking, Vol. 4, No. 3, June 1996.
	G1	Nick McKeown, "iSLIP: The iSLIP Scheduling Algorithm for Input-Queued Switches", IEEE/ACM Transactions on Networking, Vol. 7, No. 2, pp. 188-201, April 1999.
	H1	Kenneth Y. Yun, "A Terabit Multi-Service Switch With Quality Of Service Support", HOT Interconnect 8, pp. 21-30, August 2000.
	I1	Nick McKeown, "Fast Switched Backplane for a Gigabit Switched Router", Department of Engineering, Stanford University, Stanford, CA
	J1	Prabhakar et al., "Multicast Scheduling for Input-Queued Switches", IEEE Journal on Selected Areas in Communications, May 1996.
	K1	Ohsaki et al., "Performance of an Input/Output Buffered-Type ATM LAN Switch with Back-Pressure Function", IEEE/ACM Transactions on Networking, Vol. 5, No. 2, April 1997.
	L1	McKeown et al., "The Tiny Tera: A Packet Switch Core", Hot Interconnects V, Stanford University, August 1996.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.